

FIG. 1

FIG. 2 is a block diagram of a communication system 100. The system 100 includes a transmitter 138, a receiver 150, a controller 122, a memory 124, a location detector 164, a display 128, a keypad 130, a control unit 132, an antenna 134, and a speaker 136. The transmitter 138 includes an analog-to-digital converter 140, a speech coder 144, a channel coder 146, a digital signal processor 142, a phase modulator and RF amplifier 148, and a duplexer 160. The receiver 150 includes a receiver/amplifier 152, a digital signal processor 154, a digital-to-analog converter 156, and a duplexer 160. The controller 122 is connected to the memory 124 and the location detector 164. The display 128, keypad 130, control unit 132, antenna 134, and speaker 136 are also connected to the system 100.

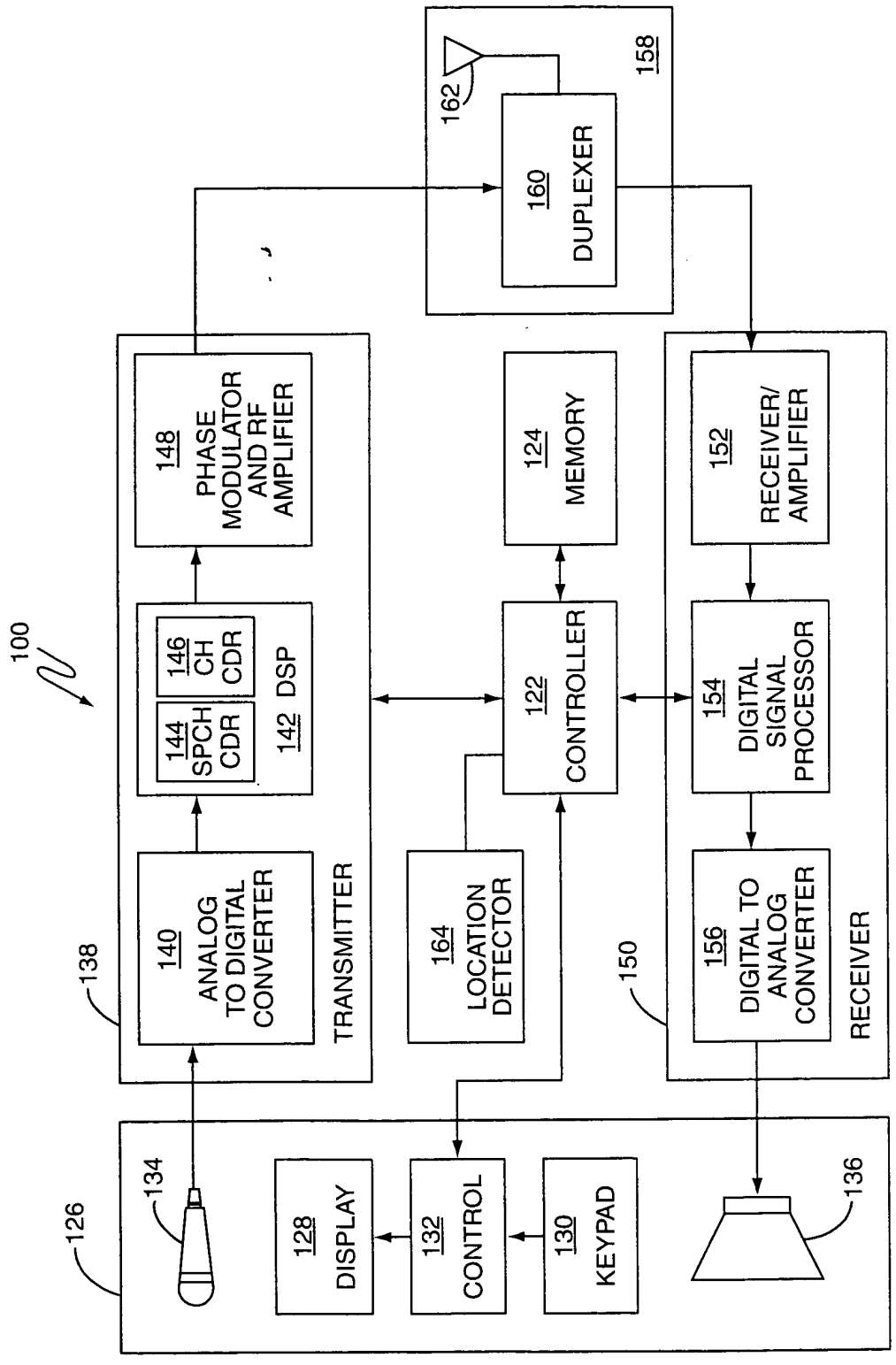


FIG. 2

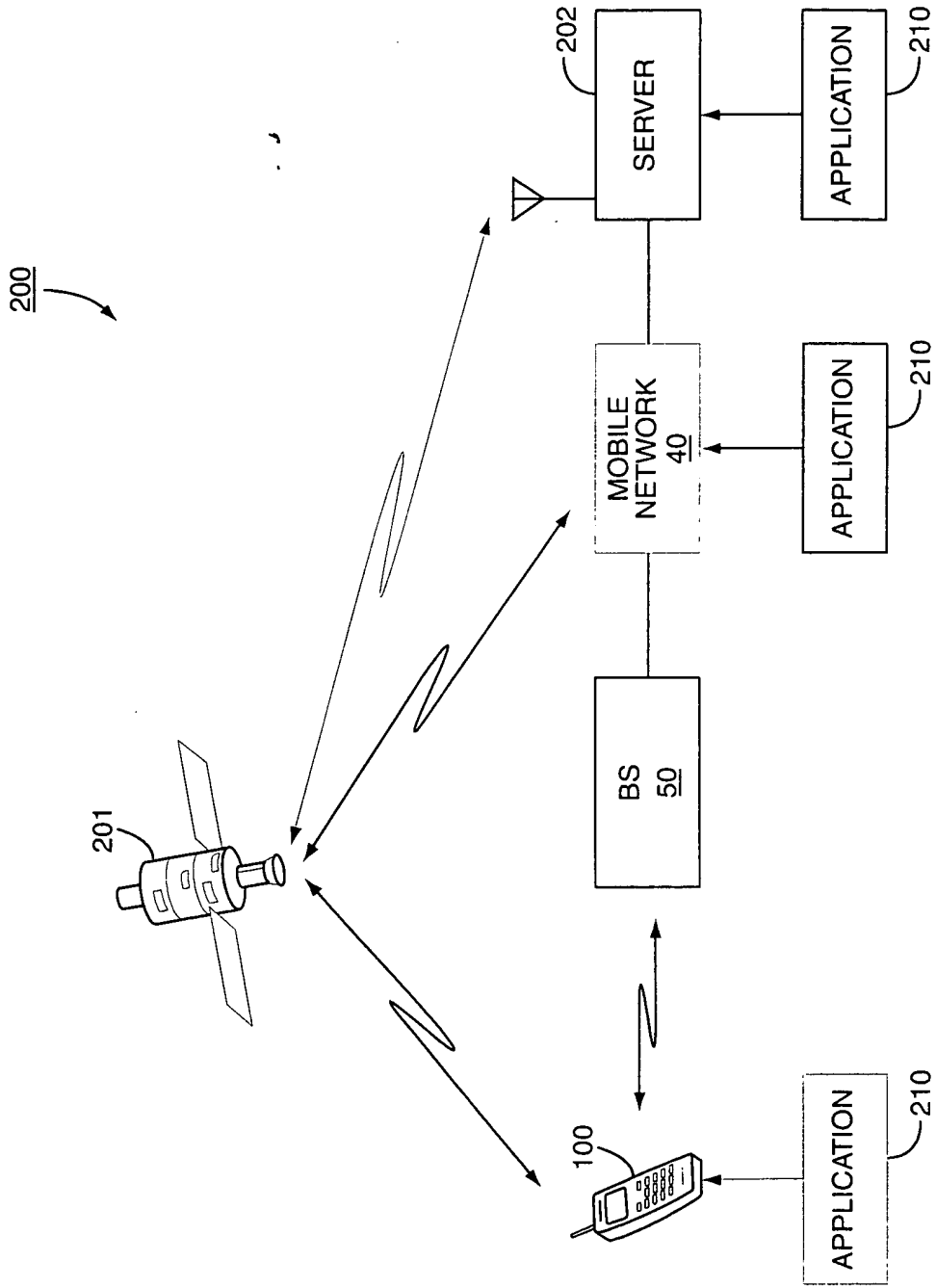


FIG. 3

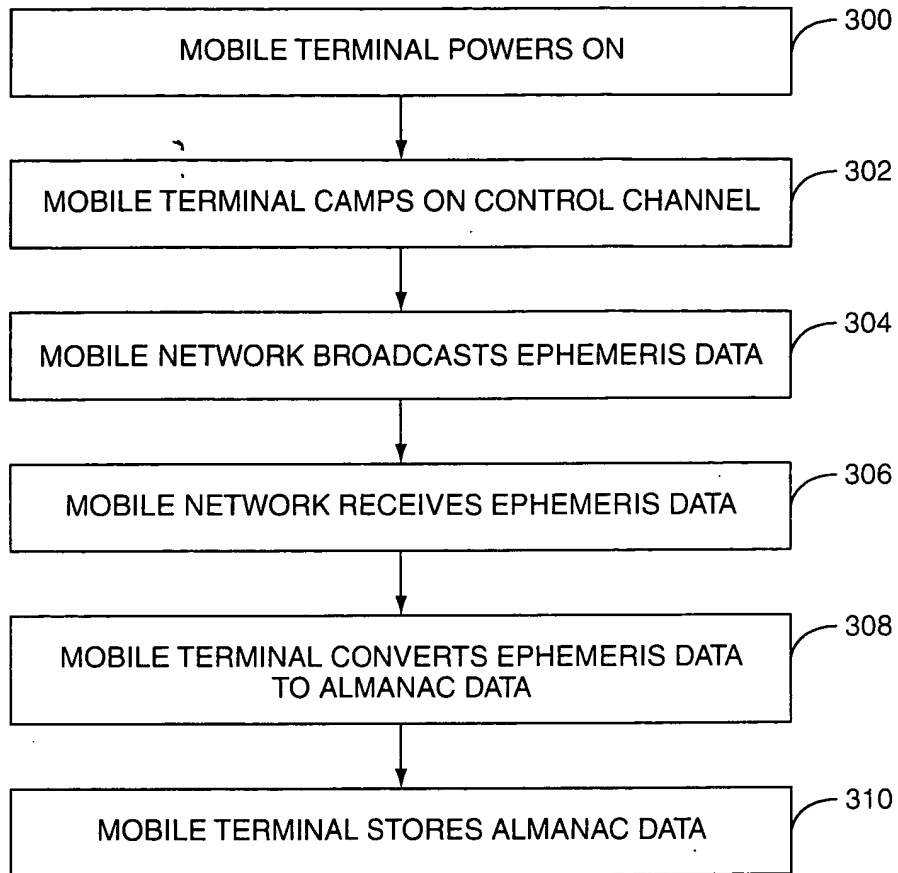


FIG. 4

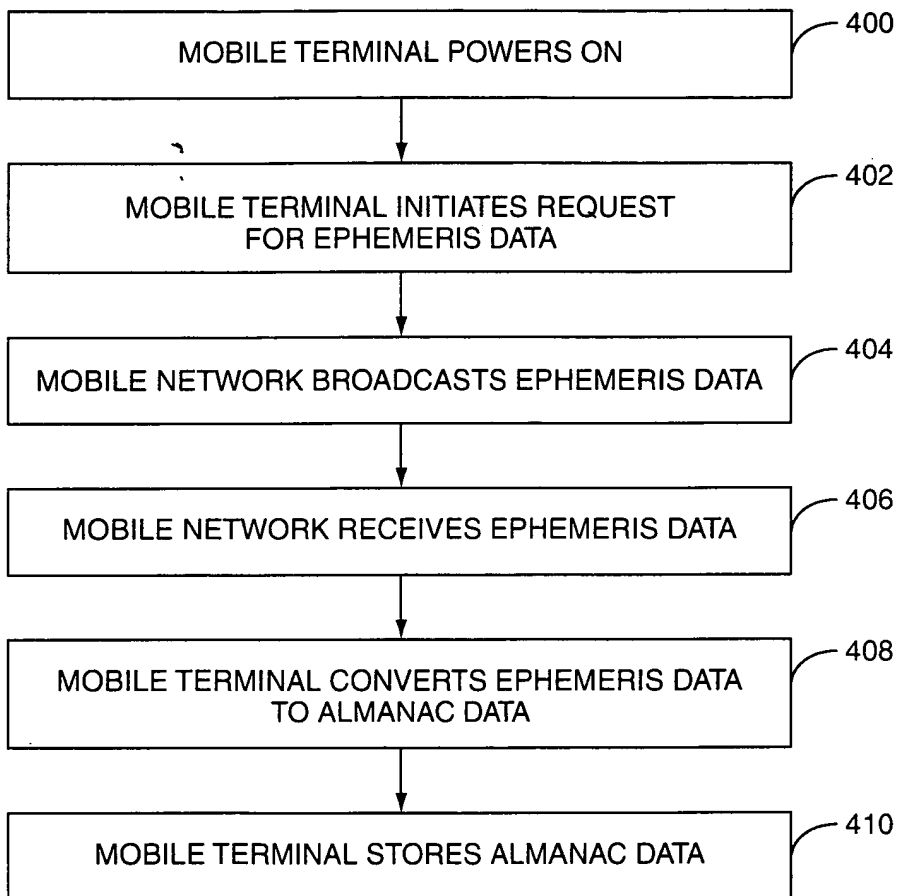


FIG. 5

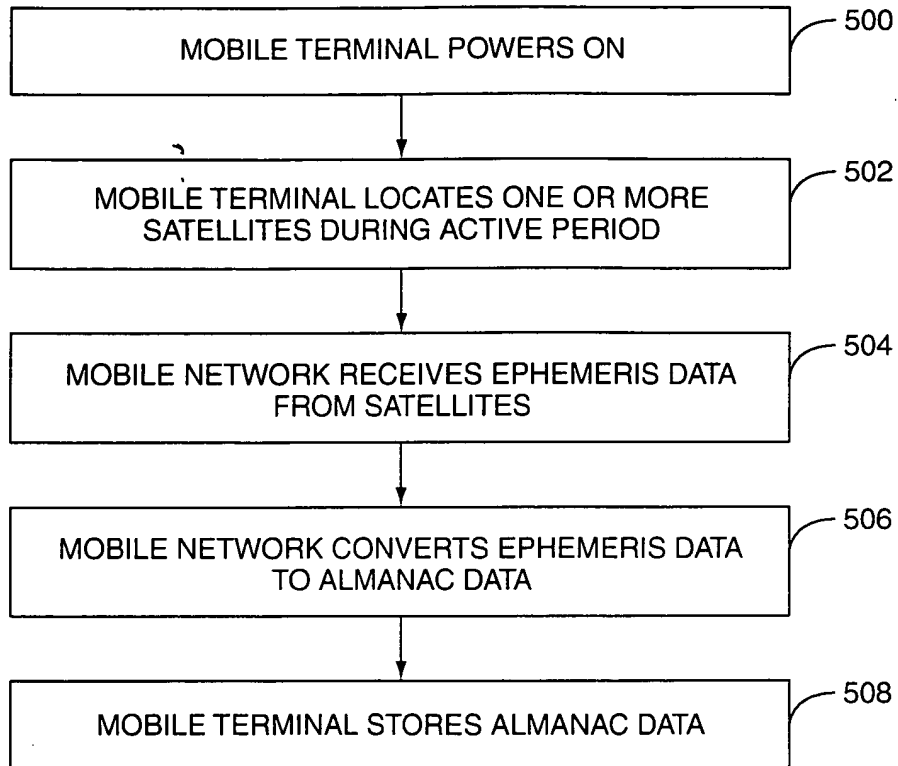


FIG 6